



In re the Application of: **Tsuyoshi YAMAMOTO et al.**

Group Art Unit: **3729**

Serial No.: **10/073,106**

Examiner: **Paul D Kim**

Filed: **February 12, 2002**

Confirmation No.: **3709**

For: **METHOD OF MOUNTING ELECTRONIC COMPONENT ON
SUBSTRATE WITHOUT GENERATION OF VOIDS IN BONDING
MATERIAL**

Attorney Docket Number: **020154**

Customer Number: **38834**

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Date: February 28, 2005

Sir:

This paper is submitted in response to the Official Action dated January 27, 2005.

Amendments to the claims begin on page 2.

Remarks begin on page 6.